Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A computer system comprising:

initialization memory containing initialization code,

a processor coupled to said initialization memory for executing said initialization code, and

a static random access memory ("SRAM") coupled to said processor and used to store variables used by the processor while executing said initialization code;

wherein said SRAM continues to receive power even if said system is otherwise powered off, said SRAM thereby being available for use by the processor without needing to subsequently be initialized when the processor executes the initialization code.

2. (Original) The apparatus of Claim 1 wherein said static random access memory is assigned addresses overlaying a portion of the addresses assigned to said initialization memory.

3. (Original) The apparatus of Claim 2 further including logic for selecting the initialization memory when the processor needs to read initialization code and for selecting the static random access memory when the processor needs to read or write to random access memory.

4. (Original) The apparatus of Claim 1 further including dynamic random access memory coupled to said processor, wherein said initialization code is for initializing said dynamic random access memory.

5. (Original) The apparatus of Claim 4 wherein said processor uses primarily only said dynamic random access memory when executing application code.

6. (Currently Amended) A method for operating a computer system comprising;

providing initialization software in ~~a~~ an initialization memory coupled to a processor;

providing static random access memory coupled to the processor;

providing power to said static random access memory even if the computer system is otherwise powered off; and

executing the initialization code in the processor while using the static random access memory to store and retrieve variables needed by the code without having to initialize the static random access memory.

7. (Original) The method of Claim 6 wherein said computer system includes dynamic random access memory and said initialization code is for initializing said dynamic random access memory.

8. (Original) The method of Claim 7 further including using primarily only said dynamic random access memory when executing application code in said processor.

9. (Previously presented) A computer system comprising:

dynamic random access memory,

initialization memory containing initialization code for initializing the dynamic random access memory at system startup, and

a static random access memory functional at system startup for storing variables used for initializing the dynamic random access memory.

10. (Original) The system of Claim 9, further including:

a processor coupled to said initialization memory for executing said initialization code upon system startup and coupled to said static random access memory for use in executing said code.

11. (Original) The system of Claim 10 wherein said processor is coupled to said static random access memory after system startup for use in executing system code other than said initialization code.

12. (Original) The system of Claim 9 wherein said static random access memory is assigned addresses overlaying a portion of the address space assigned to said initialization memory.

13. (Original) The system of Claim 12 further including means for selecting said static random access memory when said processor is executing said initialization code.

14. (Previously presented) A computer system comprising:

dynamic random access memory,

initialization memory containing initialization code for initializing the dynamic random access memory at system startup,

a processor coupled to said initialization memory for executing said initialization code,

a static random access memory coupled to said processor and used to store variables used for initializing the dynamic random access memory while executing said initialization code, said static random access memory connected to and powered by a system power supply which remains active whenever AC power is supplied to the computer system.

15. (Previously presented) The apparatus of Claim 14 wherein said static random access memory is assigned addresses overlaying a portion of the addresses assigned to said initialization memory.

16. (Previously presented) The apparatus of Claim 15 further including logic for selecting the initialization memory when the processor needs to read initialization code and for selecting the static random access memory when the processor needs to read or write to random access memory.